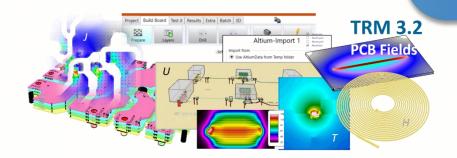


PCB & PBA Field Solver

THERMAL RISK MANAGEMENT IN ELECTRONICS



TRM

Your Software for Thermal Analysis of Printed Circuit Boards and Assemblies

Components and currents heat the circuit board - but how hot will it get?

Will it meet the temperature limits?

You think a look at the data sheet or an AppNote is enough? ...

... Never!

No data sheet in the world will be able to tell you the temperature for your board an your layout!

Our software TRM can do it and is taking pictures.

And it's easier than you think!

Components >

CAD Import >

Input >

Current >

Voltage drop >

Time dependent >

Inductance >

Technology >

Special setups >

Contact >



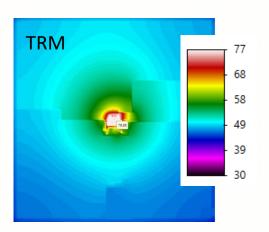
THERMAL RISK MANAGEMENT IN ELECTRONICS

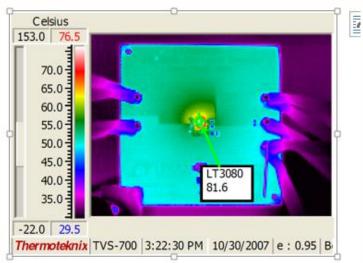
PCB & PBA Field Solver

> Components

Heat distribution is calculated 3D in the entire volume of the PCB.

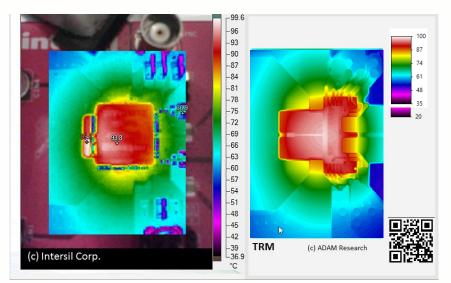
All layers, all prepregs, all via. Consistent with measurement.





https://www.analog.com/media/en/dsp-documentation/evaluation-kit-manuals/dc995A.pdf

Cooling via convection, heat sink or cold plate



http://www.intersil.com/content/dam/Intersil/documents/an19/an1922.pdf

https://www.adam-research.de/pdfs/TRM_CaseStudy1.pdf

Material data is stored in a customizable material database

How much power loss results in which temperature?

Which temperature corresponds to which power loss?

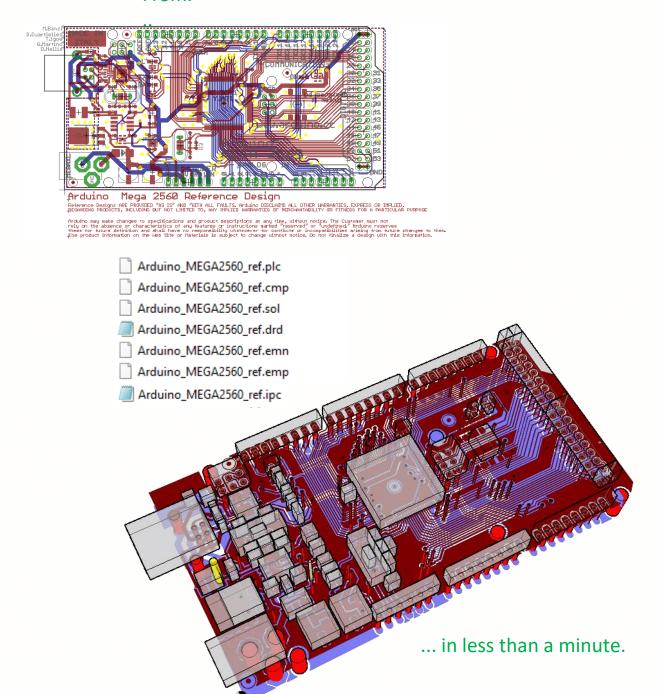


THERMAL RISK MANAGEMENT IN ELECTRONICS

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> CAD Import 1/2

From.





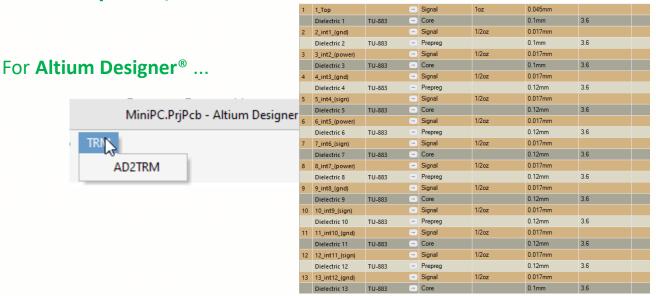
.SchDoc

TRM 3

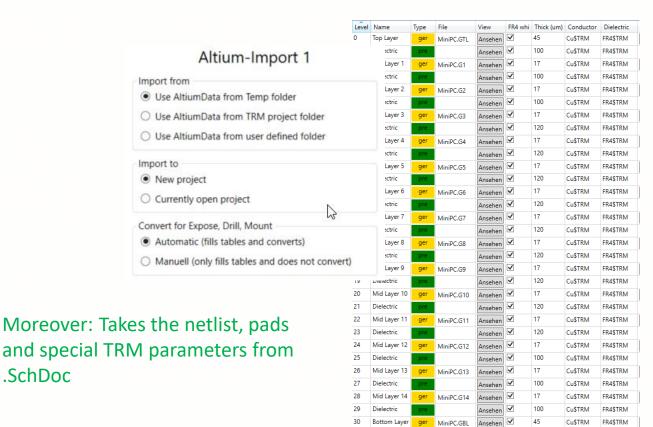
THERMAL RISK MANAGEMENT IN ELECTRONICS

PCB & PBA Field Solver

> CAD Import 2/2



... With wizard even more comfortable



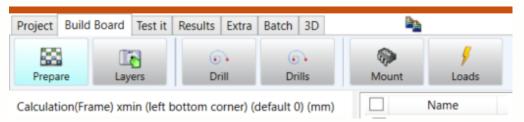


THERMAL RISK MANAGEMENT IN ELECTRONICS

PCB & PBA Field Solver

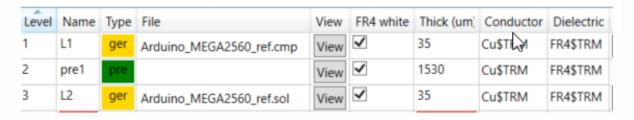
> Input

The user interface follows the production process



TRM offers something for layout beginners and experts.

Import layer stack, your Gerber, placement and drill files into TRM and add current and power dissipation. Manually or via xls.



Each drilled hole can be edited

SYNC~J2-1

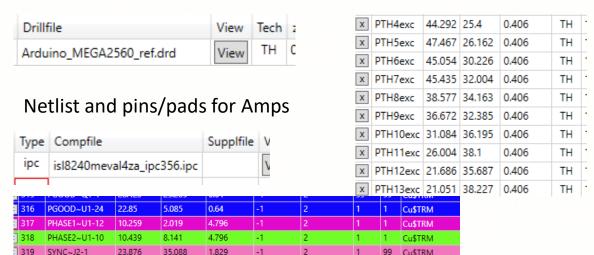
SYNC~RFSET-2

23.876

35.088

14 732

1.829



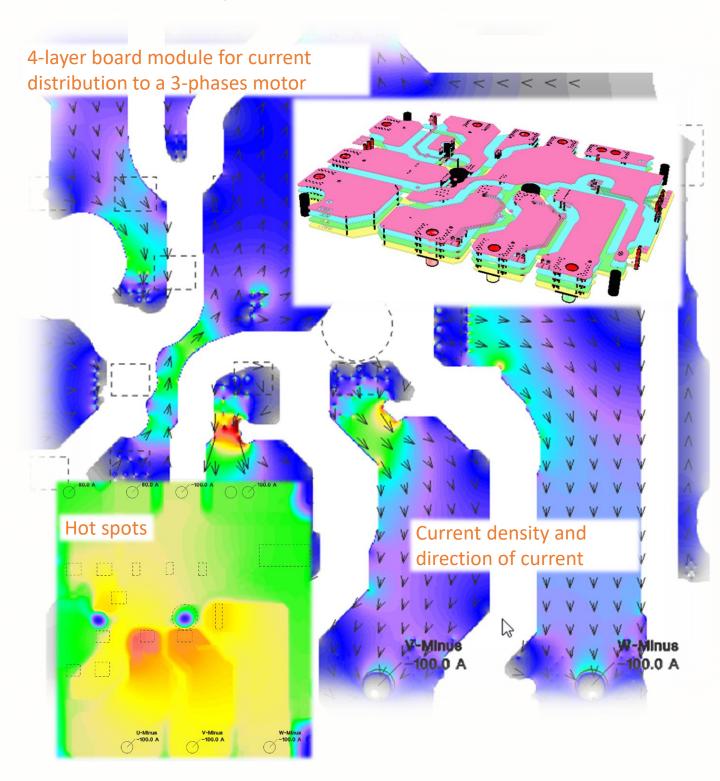
Cu\$TRM



THERMAL RISK MANAGEMENT IN ELECTRONICS

PCB & PBA Field Solver

> Current and Temperature

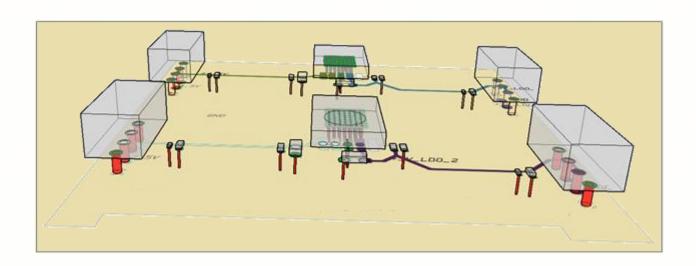


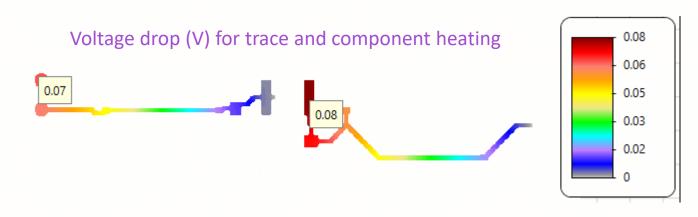


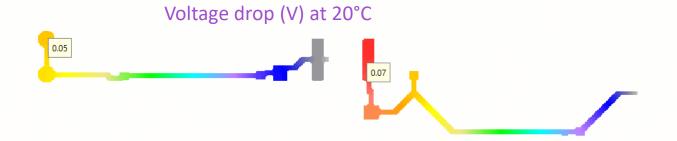
THERMAL RISK MANAGEMENT IN ELECTRONICS

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> Voltage Drop and Temperature





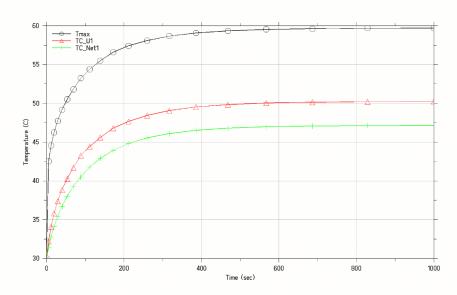


THERMAL RISK MANAGEMENT IN ELECTRONICS

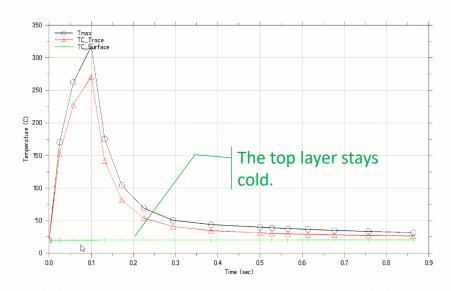
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> Transient

Heating curve with permanent losses and currents



100 ms current surge in an inner layer





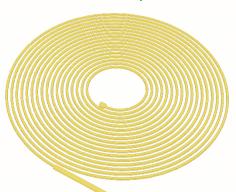
THERMAL RISK MANAGEMENT IN ELECTRONICS

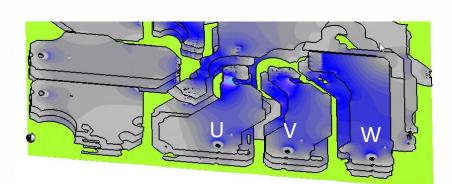
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> Inductance (Inductivity)



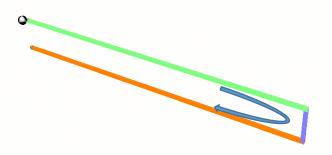
EM software: 5.9 μH Measured: 5.07 μH

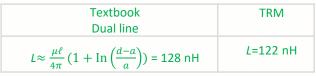


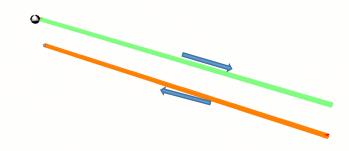


$$\mathbf{L} = \begin{pmatrix} 9.4 & 5.1 & -7.4 \\ 5.1 & 17 & -5.7 \\ -7.4 & -5.7 & 38 \end{pmatrix} \text{nH}$$

Comparison with textbook







Textbook	TRM
Single line $Lself \approx \frac{\mu\ell}{2\pi} \left(\ln \left(\frac{2\ell}{w+t} \right) + \frac{1}{2} \right) = 102$	L ₁₁ =L ₂₂ = 102 nH
	Self- and counter-inductance $\mathbf{L} = \begin{pmatrix} 102 & -41 \\ -41 & 102 \end{pmatrix} \text{nH}$
Dual line $L \approx \frac{\mu\ell}{4\pi} (1 + \ln\left(\frac{d-a}{a}\right)) = 128 \text{ nH}$	Total of matrix elements 102+102-41-41= 122 nH



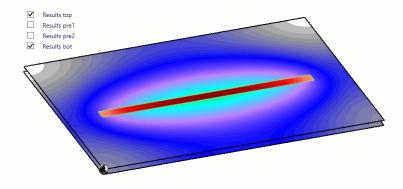
THERMAL RISK MANAGEMENT IN ELECTRONICS

PCB & PBA Field Solver

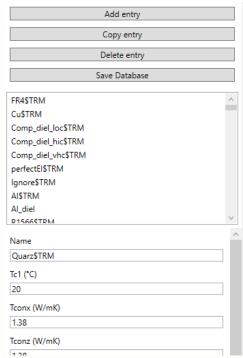
> Technology

TRM is also for purely technological investigations: even without layout data.

IPC-2221 like trace with copper flooding



Materials database



Going in depth with tables

Report by levels 1 Top Layer 0.088 W 2 Dielectric 0.000 W 3 Mid Layer 1 0.020 W 4 Dielectric 0.001 W 5 Mid Layer 2 0.014 W 6 Dielectric 0.000 W 7 Bottom Layer 0.004 W Total Joule Heat= 0.127 W

Batch runs

Pro	oject I	Build Bo	ard Test it	Results	Extra	Batch	3D	<u> </u>
	Start		Cancel					
		Index		Column	Currer	nt value	Batch values	
X	Loads	2	net1~IN	Ampere	-11		-9.00;-13.00;-1	5.00
X	Loads	3	net1~OUT	Ampere	11		9.00;13.00;15.0	0

Report	of	Electric	Results	per	Net
Net			Flux(A))>0	Vmin(V)

1 AGND 10.000 0.000 Total Heat Vmax(V) 0.013 Vdrop(mV) 12.960 RDC(mOhm) 1.296

PJoule(W) 0.127 0.127

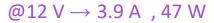


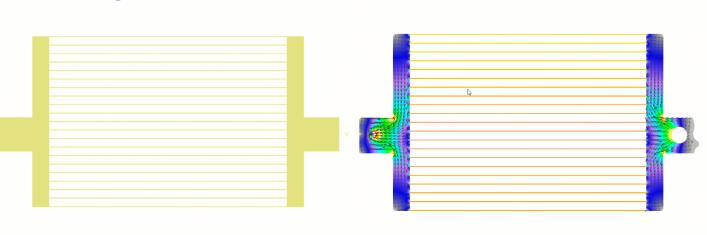
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> Special Setups

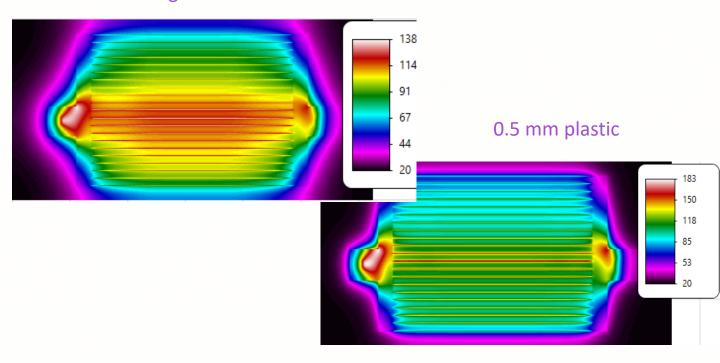






Heating foil on substrates

0.5 mm glass





THERMAL RISK MANAGEMENT IN ELECTRONICS

PCB & PBA Field Solver

TRM is more than just software

Software cannot do the thinking for you ...

... but by watching and experimenting you will learn a lot about your board and the technologies used

- How to do thermal design
- Weak points
- Alternatives
- Savings potential
- Ideas for the next design

Beyond that

- See and understand Thermal Management
- Test the benefits of new products
- Learn how to read and evaluate thermal datasheets and AppNotes

Contact to evaluate:

support@adam-research.com
www.adam-research.com

ADAM Research Calculations and Services Dr. Johannes Adam Theodor-Heuss-Strasse 12 69181 Leimen, Germany

or distributors in various countries